

Notice of References Cited	Application/Control No. 10/004,753	Applicant(s)/Patent Under Reexamination KHAN ET AL.	
	Examiner David G. Cervetti	Art Unit 2136	Page 1 of 3

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,751,723	06-2004	Kundu et al.	712/36
	B	US-6,816,750	11-2004	Klaas, Jeff	700/121
	C	US-6,754,804	06-2004	Hudepohl et al.	712/34
	D	US-6,853,385	02-2005	MacInnis et al.	345/629
	E	US-6,768,774	07-2004	MacInnis et al.	375/240.15
	F	US-6,573,905	06-2003	MacInnis et al.	345/629
	G	US-6,661,422	12-2003	Valmiki et al.	345/530
	H	US-6,798,420	09-2004	Xie, Xiaodong	345/554
	I	US-5,724,356	03-1998	Parameswaran Nair et al.	370/401
	J	US-6,763,018	07-2004	Puthiyandyil et al.	370/352
	K	US-6,580,793	06-2003	Dunn et al.	379/406.04
	L	US-6,057,555	05-2000	Reedy et al.	257/9
	M	US-6,640,239	10-2003	Gidwani, Sanjay M.	709/203

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	M. Weifi, F. Engel, and G. P. Fettweis. A New Scalable DSP Architecture for System on Chip (soc) Domains. In IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP), 1999.
	V	Lyonnard et al., Automatic generation of application-specific architectures for heterogeneous multiprocessor system-on-chip, June 2001, ACM Press, Pages: 518 - 523, Annual ACM IEEE Design Automation Conference.
	W	Cuviello et al., Fault modeling and simulation for crosstalk in system-on-chip interconnects, Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM International Conference on , 7-11 Nov. 1999, Pages:297 - 303
	X	Papachristou et al., Microprocessor based testing for core-based system on chip, 1999, ACM Press, Annual ACM IEEE Design Automation Conference, Proceedings of the 36th ACM/IEEE conference on Design automation, Pages: 586 - 591, ISBN:1-58133-109-7

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/004,753	Applicant(s)/Patent Under Reexamination KHAN ET AL.	
	Examiner David G. Cervetti	Art Unit 2136	Page 2 of 3

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,883,396	03-1999	Reedy et al.	257/9
	B	US-5,663,570	09-1997	Reedy et al.	257/9
	C	US-6,580,727	06-2003	Yim et al.	370/463
	D	US-5,363,404	11-1994	Kotzin et al.	370/335
	E	US-5,594,784	01-1997	Velius, George A.	379/88.02
	F	US-4,914,692	04-1990	Hartwell et al.	379/406.03
	G	US-5,999,525	12-1999	Krishnaswamy et al.	370/352
	H	US-6,134,578	10-2000	Ehlig et al.	718/100
	I	US-6,807,167	10-2004	Chakrabarti et al.	370/352
	J	US-6,574,217	06-2003	Lewis et al.	370/352
	K	US-6,738,358	05-2004	Bist et al.	370/289
	L	US-6,826,187	11-2004	Hey et al.	370/395.6
	M	US-6,047,372	04-2000	Thayer et al.	712/222

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Picone et al., Enhancing the performance of speech recognition with echo cancellation, 11-14 April 1988, Acoustics, Speech, and Signal Processing, 1988. ICASSP-88., Pages: 529 - 532 vol.1
	V	Wang et al., Hardware/software instruction set configurability for system-on-chip processors, June 2001, ACM Press, Annual ACM IEEE Design Automation Conference, Pages: 184 - 188, ISBN:1-58113-297-2
	W	Lahiri et al., Fast performance analysis of bus-based system-on-chip communication architectures, 7-11 Nov. 1999, Pages: 566 - 572, Computer-Aided Design, 1999. Digest of Technical Papers. 1999 IEEE/ACM International Conference on.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

Notice of References Cited	Application/Control No. 10/004,753	Applicant(s)/Patent Under Reexamination KHAN ET AL.	
	Examiner David G. Cervetti	Art Unit 2136	Page 3 of 3

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,650,649	11-2003	Muhammad et al.	370/402
	B	US-6,751,224	06-2004	Parruck et al.	370/395.6
	C	US-6,807,167	10-2004	Chakrabarti et al.	370/352
	D	US-5,991,308	11-1999	Fuhrmann et al.	370/395.53
	E	US-6,631,130	10-2003	Roy et al.	370/352
	F	US-6,636,515	10-2003	Roy et al.	370/395.1
	G	US-6,697,345	02-2004	Corrigan et al.	370/330
	H	US-6,747,977	06-2004	Smith et al.	370/395.64
	I	US-6,810,039	10-2004	Parruck et al.	370/395.5
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.